



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
www.uspto.gov

BIB DATA SHEET

CONFIRMATION NO. 6903

SERIAL NUMBER	FILING or 371(c) DATE RULE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
10/579,911		365	2827	XA-10573

APPLICANTS

Satoru Hanzawa, Hachioji, JAPAN;
 Junji Shigeta, Fuchu, JAPAN;
 Shinichiro Kimura, Kunitachi, JAPAN;
 Takeshi Sakata, Hino, JAPAN;
 Riichiro Takemura, Tokyo, JAPAN;
 Kazuhiko Kajigaya, Iruma, JAPAN;

**** CONTINUING DATA *******

This application is a 371 of PCT/JP03/14901 11/21/2003

/FH/, 6/15/09

**** FOREIGN APPLICATIONS *******

None, /FH/, 6/15/09

**** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ****

08/18/2007

Foreign Priority claimed	<input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119(a-d) conditions met	<input type="checkbox"/> Yes <input checked="" type="checkbox"/> No		JAPAN	23	9	3
Verified and Acknowledged	/FERNANDO N HIDALGO/ Examiner's Signature	Initials				

ADDRESS

MILES & STOCKBRIDGE PC
 1751 PINNACLE DRIVE
 SUITE 500
 MCLEAN, VA 22102-3833
 UNITED STATES

TITLE

Semiconductor Integrated Circuit Device

FILING FEE RECEIVED 1030	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)
		<input type="checkbox"/> Other _____
		<input type="checkbox"/> Credit